# LCD / LCM SPECIFICATION





# 曜 凌 光 電 股 份 有 限 公 司 Raystar Optronics, Inc.

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### **RX12864H-BIW**

### **SPECIFICATION**

### **CUSTOMER:**

| APPROVED BY |
|-------------|
| PCB VERSION |
| DATE        |

#### FOR CUSTOMER USE ONLY

| SALES BY | APPROVED BY | CHECKED BY | PREPARED BY |
|----------|-------------|------------|-------------|
|          |             |            |             |
|          |             |            |             |
|          |             |            |             |

Release DATE:



# **Revision History**

| VERSION | DATE       | REVISED PAGE NO. | Note                    |
|---------|------------|------------------|-------------------------|
| 0       | 2011/10/25 |                  | First issue             |
| Α       | 2011/12/27 |                  | Modify LCM drawing      |
| В       | 2012/10/22 |                  | Modify note3            |
| С       | 2012/11/29 |                  | Modify Backlight        |
|         |            |                  | Information             |
| D       | 2014/10/13 |                  | Remove IC               |
|         |            |                  | information             |
| E       | 2016/02/25 |                  | Modify Precautions in   |
|         |            |                  | use of LCD Modules      |
|         |            |                  | & Static electricity    |
|         |            |                  | test                    |
| F       | 2016/11/28 |                  | Add FPC bending         |
|         |            |                  | rule                    |
| G       | 2019/09/29 |                  | Modify Material List of |
|         |            |                  | Components for          |
|         |            |                  | RoHs                    |
| Н       | 2019/12/20 |                  | Modify Precautions in   |
|         |            |                  | use of LCD Modules      |
| I       | 2020/06/18 |                  | Modify Contour          |
|         |            |                  | Drawing.                |



# **Contents**

- 1.General Specification
- 2. Module Classification Information
- 3.Interface Pin Function
- 4. Contour Drawing & Block Diagram
- 5. Optical Characteristics
- 6. Absolute Maximum Ratings
- 7. Electrical Characteristics
- 8.Backlight Information
- 9. Reliability
- 10.Inspection specification
- 11.Precautions in use of LCD Modules
- 12. Material List of Components for RoHs
- 13.Recommendable Storage



# 1.General Specification

The Features is described as follow:

■ Module dimension: 80.0 x 54.0 x 9.5 mm

■ View area: 70.7 x 38.8 mm

Active area: 66.52 x 33.24 mm

■ Number of dots: 128 x 64

■ Dot size: 0.48 x 0.48 mm

■ Dot pitch: 0.52 x 0.52 mm

■ LCD type: STN Negative, Blue Transmissive

■ Duty: 1/65 , 1/9 Bias

■ View direction: 6 o'clock

■ Backlight Type: LED, White

■ IC: ST7567-G



### 2. Module Classification Information

| <u>R</u> | <u>X</u> | <u>12864</u> | <u>H</u> | _ | <u>B</u> | <u> </u> | W |
|----------|----------|--------------|----------|---|----------|----------|---|
| ①        | 2        | 3            | 4        |   | (5)      | 6        | 7 |

| Item | Description    |                           |       |                             |                       |  |  |
|------|----------------|---------------------------|-------|-----------------------------|-----------------------|--|--|
| 1    | R: Raystar O   | R: Raystar Optronics Inc. |       |                             |                       |  |  |
| 2    | Display        | C: Character Type,        |       | T:TAB Type                  |                       |  |  |
|      | Display        | G: Graphic Type           |       | X:COG Type                  |                       |  |  |
| 3    | Display Font : | 128 * 64 dot              |       |                             |                       |  |  |
| 4    | Serials code:  |                           |       |                             |                       |  |  |
|      |                | P→TN Positive, Gray       |       | V→FSTN Ne                   | egative, Blue         |  |  |
|      |                | N→TN Negative,            |       | T→FSTN Ne                   | egative, Black        |  |  |
|      |                | L→VA Negative             |       | D→FSTN N                    | egative (Double film) |  |  |
|      |                | H→ HTN Positive, Gray     |       | F→FSTN Po                   | ositive               |  |  |
| 5    | LCD            | I→HTN Negative, Black     |       | K→FSC Neg                   | gative                |  |  |
|      |                | U→HTN Negative, Blue      |       | S→FSC Pos                   | sitive                |  |  |
|      |                | B→STN Negative, Blue      |       | E→ISTN Ne                   | gative, Black         |  |  |
|      |                | G→STN Positive, Gray      |       | C→CSTN Negative, Black      |                       |  |  |
|      |                | Y→STN Positive, Yellow    | Green | A→ASTN Negative, Black      |                       |  |  |
|      |                | A: Reflective, N.T, 6:00  |       | K: Transflective, W.T,12:00 |                       |  |  |
|      | Polarizer      | D: Reflective, N.T, 12:0  | 0     | 1: Transflective, U.T,6:00  |                       |  |  |
|      | Type,          | G: Reflective, W. T, 6:00 | )     | 4: Transflective, U.T.12:00 |                       |  |  |
|      | Temperature    | J: Reflective, W. T, 12:0 |       | C: Transmissive, N.T,6:00   |                       |  |  |
| 6    | range,         | 0: Reflective, U. T, 6:00 |       | F: Transmissive, N.T,12:00  |                       |  |  |
|      | 90,            | 3: Reflective, U. T, 12:0 | 0     | I: Transmissive, W. T, 6:00 |                       |  |  |
|      | View           | B: Transflective, N.T,6:0 | 00    |                             | ssive, W.T,12:00      |  |  |
|      | direction      | E: Transflective, N.T.12  |       | 2: Transmissive, U. T, 6:00 |                       |  |  |
|      |                | H: Transflective, W.T,6:  |       |                             | sive, U.T,12:00       |  |  |
|      |                | N→ Without backlight      | W→LE  | ,                           | G→LED, Green          |  |  |
|      |                | P→EL, Blue                | A→LED |                             | S→LED, Full color     |  |  |
|      |                | T→EL, Green               | R→LED |                             | J→DIP LED, Blue       |  |  |
| 7    | Backlight      | D→EL, White               |       | ), Orange                   | K→DIP LED, White      |  |  |
|      |                | M→EL, Yellow Green        | B→LED |                             | E→DIP LED, Yellow     |  |  |
|      | 7              | F→CCFL, White             |       | , Dual color                | L→DIP LED, Amber      |  |  |
|      |                | Y→LED, Yellow Green       | C→LED | , Full color                | I→DIP LED, Red        |  |  |
|      |                |                           |       |                             |                       |  |  |



# 3.Interface Pin Function

| Pin No. | Symbol | Level   | Description   |                            |                                      |  |  |  |
|---------|--------|---------|---|----------------------------|--------------------------------------|--|--|--|
| 1       | PSB    | I       | PSB selects the interface type: Serial or Parallel.       |                            |                                      |  |  |  |
|         |        |         | C86 selects the microprocessor type in parallel interface |                            |                                      |  |  |  |
|         |        |         | mode.   |                            |                                      |  |  |  |
|         |        |         | PSB   | PSB C86 Selected Interface |                                      |  |  |  |
|         |        |         | "H"   | "H"                        | Parallel 6800 Series MPU             |  |  |  |
|         |        |         | 11  | 11                         | Interface                            |  |  |  |
| 2       | C86    | ı       | "H"   | "L"                        | Parallel 8080 Series MPU             |  |  |  |
|         | 000    | '       | 11  | L                          | Interface                            |  |  |  |
|         |        |         | "L"   | "X"                        | Serial 4-Line SPI Interface          |  |  |  |
|         |        |         | Please ref  | fer to "AP                 | PLICATION NOTES" and                 |  |  |  |
|         |        |         | "Micropro   | cessor Int                 | erface"                              |  |  |  |
|         |        |         | (Section 6) for detailed connection of the selected       |                            |                                      |  |  |  |
|         |        |         | interface.  |                            |                                      |  |  |  |
| 3       | VG     | Power   | VG is the   | LCD drivi                  | ng voltage for segment circuits.     |  |  |  |
| 4       | XV0    | Power   | XV0 is the LCD driving voltage for common circuits at     |                            |                                      |  |  |  |
|         | 7,10   | 1 OWC1  | positive fr   | ame.                       |                                      |  |  |  |
| 5       | V0     | Power   | V0 is the I   | LCD drivir                 | ng voltage for common circuits at    |  |  |  |
|         | •••    | 1 01101 | negative f  | rame.                      |                                      |  |  |  |
| 6       | VSS    |         | This is a 0   | )V termina                 | al connected to the system GND.      |  |  |  |
| 7       | VDD    |         | Shared wi   | ith the MF                 | PU power supply terminal VDD. ( 3.3  |  |  |  |
| ,       | VDD    |         | V )   |                            |                                      |  |  |  |
| 8       | D7     |         | When usi  | ing 8-bit <sub> </sub>     | parallel interface: (6800 or 8080    |  |  |  |
| 9       | D6     |         | mode)   |                            |                                      |  |  |  |
| 10      | D5     |         | 8-bit bi-dir  | ectional o                 | data bus. Connect to the data bus of |  |  |  |
| 11      | D4     |         | 8-bit microprocessor.                                     |                            |                                      |  |  |  |
| 12      | D3     |         | When CSB is non-active (CSB="H"), D[7:0] pins are high    |                            |                                      |  |  |  |
|         |        |         | impedance.  |                            |                                      |  |  |  |
| 13      | D2     |         | When using serial interface: 4-LINE                       |                            |                                      |  |  |  |
| 14      | D1     |         | D7=SDA :  | Serial da                  | ata input.                           |  |  |  |



| ٦ |    |            |   | D6=SCL : Serial clock input.   |  |  |   |     |  |  |
|---|----|------------|---|--|--|--|---|-----|--|--|
|   |    |            |   | D[5:0] are not used and should connect to "H" by VDD1  |  |  |   |     |  |  |
|   |    |            |   | or VDDH.   |  |  |   |     |  |  |
|   | 15 | D0         |   | When CSB is non-active (CSB="H"), D[7:0] pins are high   |  |  |   |     |  |  |
|   |    |            |   |  | dance.   | 11011 0  |   | "   |  |  |
|   |    |            |   | lilipe   | dance.   |  |   |     |  |  |
|   |    |            |   | Poor   | 1/1/rita av  | ooutio   | on control pin. When PSB is "H",  |     |  |  |
|   |    |            |   | —  |  | Eculic   | · · · · · · · · · · · · · · · · · · ·   |     |  |  |
|   |    |            |   | C86  | MPU Type   | ERD  | Description   |     |  |  |
|   |    |            |   |  |  |  | Read/Write control input pin.  R/W="H": When E is "H", D[7:0] are in output   |     |  |  |
|   |    |            |   | н  | 6800   | E  | mode.   |     |  |  |
|   | 16 | ERD        |   |  | series   |  | R/W="L": Signals on D[7:0] are latched at the   |     |  |  |
|   |    |            |   |  |  |  | falling edge of E signal.   |     |  |  |
|   |    |            |   | L  | 8080<br>series   | /RD  | Read enable input pin.  When /RD is "L", D[7:0] are in output mode.   |     |  |  |
|   |    |            |   |  | series   | When ACD is E , D[7.0] are in output mode.   |   |     |  |  |
|   |    |            |   | ERD  | is not use   | ed in s  | serial interface and should fix to "H"  | '   |  |  |
|   |    |            |   | by V   | DD1 or VI  | DDH.   |   |     |  |  |
|   |    |            |   | Read   | d/Write ex   | ecutio   | on control pin. When PSB is "H",  |     |  |  |
|   |    |            |   | C86  | MPU Type   | RWR  | Description   |     |  |  |
|   |    |            |   |  | 6800   |  | Read/Write control input pin.   |     |  |  |
|   |    |            |   | Н  | series   | R/W  | R/W="H": read.  |     |  |  |
|   | 17 | RWR        |   |  |  |  | R/W="L": write.   |     |  |  |
|   |    |            | - | L  | 8080   | WR   | Write enable input pin.   |     |  |  |
|   |    |            |   |  |  |  | I Signals on DI7:01 will be latched at the rising I   |     |  |  |
|   |    |            |   |  | series   | 70013  | Signals on D[7:0] will be latched at the rising<br>edge of /WR signal.  |     |  |  |
|   |    |            |   |  |  |  | edge of /WR signal.   | ,,  |  |  |
|   |    |            | 1 | RWF  | R is not us  | sed in   |   | ,,  |  |  |
|   |    |            | 1 | RWF<br>by V  | R is not us<br>DD1 or VI   | sed in<br>DDH.   | edge of MR signal.<br>serial interface and should fix to "H'  | "   |  |  |
|   |    |            |   | RWF<br>by V  | R is not us<br>DD1 or VI   | sed in<br>DDH.   | edge of /WR signal.   | "   |  |  |
|   | 18 | AO         |   | RWF<br>by V<br>It det  | R is not us<br>DD1 or VI<br>termines v   | sed in<br>DDH.<br>wheth  | edge of MR signal.  serial interface and should fix to "H' er the access is related to data or  |     |  |  |
|   | 18 | AO         |   | RWF<br>by V<br>It det<br>comi<br>A0="  | R is not us DD1 or VI termines v mand. H": Indica  | sed in DDH. whether ates the   | edge of MR signal.  serial interface and should fix to "H' er the access is related to data or nat signals on D[7:0] are display dat  | ta. |  |  |
|   | 18 | AO         |   | RWF<br>by V<br>It det<br>comi<br>A0="  | R is not us DD1 or VI termines v mand. H": Indica  | sed in DDH. whether ates the   | edge of MR signal.  serial interface and should fix to "H' er the access is related to data or  | ta. |  |  |
|   | 18 | AO         |   | RWF<br>by V<br>It det<br>comi<br>A0="  | R is not us DD1 or VI termines v mand. H": Indica  | sed in DDH. whether ates that the ates  | edge of MR signal.  serial interface and should fix to "H' er the access is related to data or nat signals on D[7:0] are display dat  | ta. |  |  |
|   | 18 | A0<br>RSTB |   | RWF<br>by V<br>It dei<br>comi<br>A0="<br>A0="  | R is not us DD1 or VI termines v mand. H": Indica  | sed in DDH. whether ates thates the  | edge of MR signal.  serial interface and should fix to "H' er the access is related to data or nat signals on D[7:0] are display dat nat signals on D[7:0] are command. It pin. When RSTB is "L", internal  | ta. |  |  |
|   | 1  |            |   | RWF<br>by V<br>It def<br>comin<br>A0="<br>A0="<br>Hard<br>initia   | R is not us DD1 or VI termines v mand. H": Indica Ware rese                                    | sed in DDH. whether ates thates the execution at the exec | edge of MR signal.  serial interface and should fix to "H' er the access is related to data or nat signals on D[7:0] are display dat nat signals on D[7:0] are command. It pin. When RSTB is "L", internal  | ta. |  |  |
|   | 1  |            |   | RWF<br>by V<br>It def<br>comi<br>A0="<br>A0="<br>Hard<br>initia  | R is not us DD1 or VI termines v mand. H": Indica L": Indica ware rese                         | sed in DDH. whether ates the et inpues executal regions  | edge of MR signal.  serial interface and should fix to "H' er the access is related to data or nat signals on D[7:0] are display dat nat signals on D[7:0] are command. It pin. When RSTB is "L", internal  | ta. |  |  |
|   | 1  |            |   | RWF<br>by V<br>It def<br>cominate A0="<br>A0="<br>Hard<br>initiand for this control of this cont | R is not us DD1 or VI termines v mand. H": Indica L": Indica ware rese lization is the interna | sed in DDH. whether the tinpues executed at the control of the con | edge of WR signal.  serial interface and should fix to "H' er the access is related to data or nat signals on D[7:0] are display dat nat signals on D[7:0] are command. It pin. When RSTB is "L", internal Ited sters will be initialized.  | ta. |  |  |
|   | 19 | RSTB       |   | RWF<br>by V<br>It det<br>cominant<br>A0="<br>Hard<br>initiant<br>and the<br>Chip<br>CSB  | R is not us DD1 or VI termines v mand. H": Indica L": Indica ware rese lization is the interna | sed in DDH. whether ates the executal region out pinen CS  | serial interface and should fix to "H' er the access is related to data or nat signals on D[7:0] are display dat nat signals on D[7:0] are command. It pin. When RSTB is "L", internal lited sters will be initialized. Interface access is enabled when SB is non-active (CSB="H"), D[7:0] | ta. |  |  |

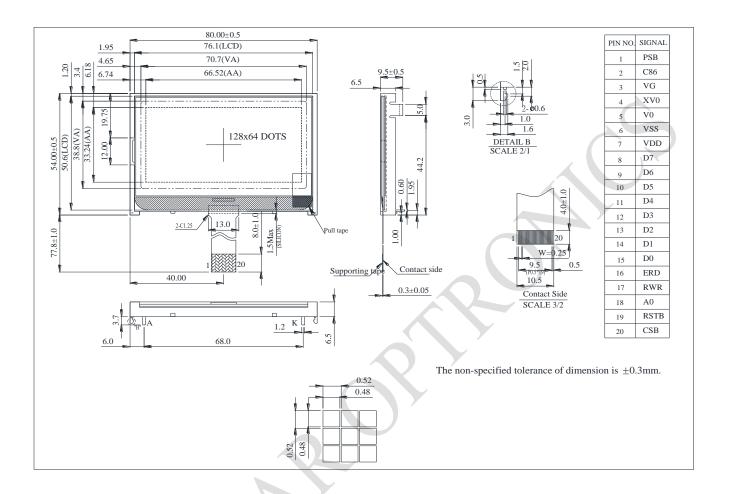


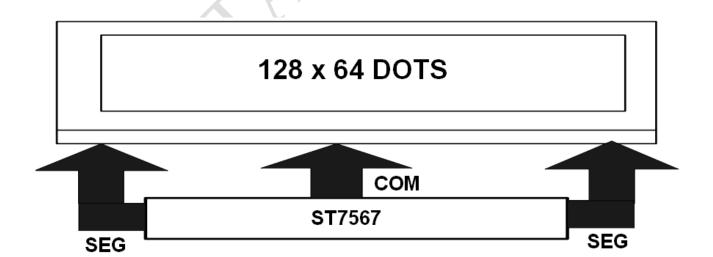
### C1=C2=1UF/0805

|         | 2 101/ |         |
|---------|--------|---------|
| PIN NO. | SIGNAL |         |
| 1       | PSB    | P3.6    |
| 2       | C86    | P3.6    |
| 3       | VG     |         |
| 4       | XV0    | C2 = C1 |
| 5       | V0     | C2 + C1 |
| 6       | VSS    | VSS     |
| 7       | VDD    | VDD     |
| 8       | D7     | P1.7    |
| 9       | D6     | P1.6    |
| 10      | D5     | P1.5    |
| 11      | D4     | P1.4    |
| 12      | D3     | P1.3    |
| 13      | D2     | P1.2    |
| 14      | D1     | P1.1    |
| 15      | D0     | P1.0    |
| 16      | ERD    | P3.4    |
| 17      | RWR    | P3.7    |
| 18      | A0     | P3.0    |
| 19      | RSTB   | P3.2    |
|         |        | D2 2    |



# 4. Contour Drawing & Block Diagram



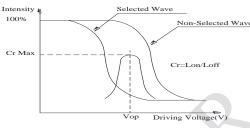


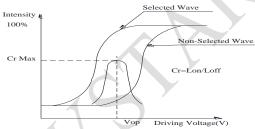


# **5.Optical Characteristics**

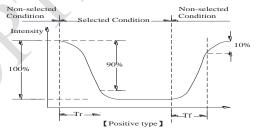
| Item           | Symbol | Condition | Min | Тур | Max | Unit    |
|----------------|--------|-----------|-----|-----|-----|---------|
|                | θ      | CR≧2      | 0   | _   | 20  | ψ= 180° |
| View Angle     | θ      | CR≧2      | 0   | _   | 40  | ψ= 0°   |
|                | θ      | CR≧2      | 0   | _   | 30  | ψ= 90°  |
|                | θ      | CR≧2      | 0   | _   | 30  | ψ= 270° |
| Contrast Ratio | CR     | _         | _   | 3   | 7   | _       |
| Decrease Time  | T rise | _         | _   | 200 | 300 | ms      |
| Response Time  | T fall | _         | Q.  | 250 | 350 | ms      |

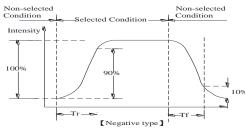
#### **Definition of Operation Voltage (Vop)**





#### **Definition of Response Time (Tr, Tf)**

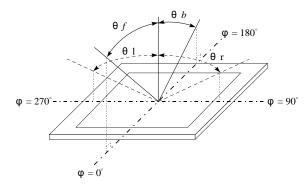




#### **Conditions:**

Frame Frequency: 64 HZ Driving Waveform: 1/N duty, 1/a bias

### **Definition of viewing angle(CR≥2)**





# **6.Absolute Maximum Ratings**

| Item                         | Symbol               | Min  | Тур | Max                  | Unit         |
|------------------------------|----------------------|------|-----|----------------------|--------------|
| Operating Temperature        | T <sub>OP</sub>      | -20  | _   | +70                  | $^{\circ}$ C |
| Storage Temperature          | T <sub>ST</sub>      | -30  | _   | +80                  | $^{\circ}$ C |
| Input Voltage                | Vı                   | -0.3 | _   | V <sub>DD</sub> +0.3 | V            |
| Digital Power Supply Voltage | V <sub>DD</sub> -Vss | -0.3 | _   | 3.6                  | ٧            |
| LCD Power supply voltage     | V0-XV0               | -0.3 | - 4 | 16                   | V            |



### 7. Electrical Characteristics

| Item                     | Symbol              | Condition             | Min                 | Тур  | Max                | Unit |
|--------------------------|---------------------|-----------------------|---------------------|------|--------------------|------|
| Supply Voltage For Logic | $V_{DD}$ - $V_{SS}$ | _                     | 3.0                 | 3.3  | 3.6                | V    |
|                          |                     | Ta=-20°ℂ              | _                   | _    | _                  | V    |
| Supply Voltage For LCM   | XV0-V0              | Ta=25°ℂ               | _                   | 10.0 | - (                | V    |
|                          |                     | Ta=70°C               | _                   | _    | 4                  | V    |
| Input High Volt.         | V <sub>IH</sub>     | _                     | 0.7V <sub>DD</sub>  | -    | $V_{DD}$           | V    |
| Input Low Volt.          | V <sub>IL</sub>     | _                     | Vss                 |      | 0.3V <sub>DD</sub> | V    |
| Output High Volt.        | V <sub>OH</sub>     | _                     | 0.8 V <sub>DD</sub> |      | $V_{DD}$           | V    |
| Output Low Volt.         | V <sub>OL</sub>     | - (                   | Vss                 | / _  | 0.2V <sub>DD</sub> | V    |
| Supply Current(No        |                     |                       | 7                   |      |                    |      |
| include                  | $I_{DD}$            | V <sub>DD</sub> =3.3V | _                   | 2.0  | _                  | mA   |
| LED Backlight)           |                     |                       |                     |      |                    |      |

Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance.



# 8.Backlight Information

### **Specification**

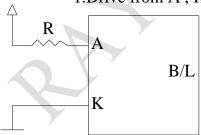
| PARAMETER                                | SYMBOL | MIN | TYP  | MAX | UNIT              | TEST CONDITION                         |
|--|--------|-----|------|-----|-------------------|--|
| Supply Current                           | ILED   | _   | 96   | 120 | mA                | V=3.5V                                 |
| Supply Voltage                           | V      | 3.3 | 3.5  | 3.7 | v                 | - 4                                    |
| Reverse Voltage                          | VR     | _   | _    | 5   | v                 | - 1                                    |
| Luminance<br>(Without LCD)               | IV     | 840 | 1050 | _   | CD/M <sup>2</sup> | ILED=96mA                              |
| LED Life Time<br>(For Reference<br>only) | _      | _   | 50K  | 2   |                   | ILED=96mA<br>25℃,50-60%RH,<br>(Note 1) |
| Color                                    | White  |     |      |     |                   |  |

Note: The LED of B/L is drive by current only, drive voltage is for reference only. drive voltage can make driving current under safety area (current between minimum and maximum).

Note 1:50K hours is only an estimate for reference.

LED B\L Drive Method

1.Drive from A, K





# 9. Reliability

#### Content of Reliability Test (Wide temperature, -20°C~70°C)

| Environmental Test                    |  |   |      |  |  |
|---------------------------------------|--|---|------|--|--|
| Test Item                             | Content of Test  | Test Condition  | Note |  |  |
| High Temperature storage              | Endurance test applying the high storage temperature for a long time.  | 200hrs  | 2    |  |  |
| Low Temperature storage               | Endurance test applying the low storage temperature for a long time.   | -30℃<br>200hrs  | 1,2  |  |  |
| High Temperature<br>Operation         | Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.   | 70℃<br>200hrs   |      |  |  |
| Low Temperature<br>Operation          | Endurance test applying the electric stress under low temperature for a long time.   | -20°ℂ<br>200hrs   | 1    |  |  |
| High Temperature/<br>Humidity storage | The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature. | 60°C,90%RH<br>96hrs   | 1,2  |  |  |
| Thermal shock resistance              | The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C  30min 5min 30min 1 cycle  | -20℃/70℃<br>10 cycles   |      |  |  |
| Vibration test                        | Endurance test applying the vibration during transportation and using.   | Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes | 3    |  |  |
| Static electricity test               | Endurance test applying the electric stress to the terminal.   | VS=±600V(contact),<br>±800v(air),<br>RS=330Ω<br>CS=150pF<br>10 times  |      |  |  |

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.



# 10.Inspection specification

| NO | Item   |   |                             | Criterion  |   | AQL  |
|----|--|---|-----------------------------|--|---|------|
| 01 | Electrical<br>Testing                            | defect. 1.2 Missing character, dot 1.3 Display malfunction. 1.4 No function or no displa      |                             | olay.<br>exceeds product specifications.   |   | 0.65 |
|    |  | 1.7 Mixed produ<br>1.8 Contrast def   | • •                         |  |   |      |
| 02 | Black or white<br>spots on LCD<br>(display only) | 2.1 White and black spots of three white or black spots 2.2 Densely spaced: No more           |                             | pots present.  | <b>\</b>  | 2.5  |
| 03 | LCD black<br>spots, white<br>spots,              | 3.1 Round type  Φ=( x + y ) /   | <sup>2</sup><br>↓ Y         | SIZE   | Acceptable Q TY Accept no dense 2 1 0           | 2.5  |
|    | contamination<br>(non-display)                   | 3.2 Line type : (/  | Length L≦3.0 L≦2.5          | $\begin{array}{c} \text{Width} \\ \text{W} \! \leq \! 0.02 \\ 0.02 \! < \! \text{W} \! \leq \! 0.03 \\ 0.03 \! < \! \text{W} \! \leq \! 0.05 \\ 0.05 \! < \! \text{W} \end{array}$ | Acceptable Q TY Accept no dense 2 As round type | 2.5  |
| 04 | Polarizer<br>bubbles                             | If bubbles are visible judge using black specifications, noto find, must chespecify direction | k spot<br>ot easy<br>eck in | Size Φ $Φ \le 0.20$ $0.20 < Φ \le 0.50$ $0.50 < Φ \le 1.00$ $1.00 < Φ$ $Total Q TY$  | Acceptable Q TY Accept no dense 3 2 0 3         | 2.5  |



| NO | Item      | Criterion A  |                              |                          |     |  |  |
|----|-----------|--|------------------------------|--------------------------|-----|--|--|
| 05 | Scratches | Follow NO.3 LCD black spots, white spots, contamination                    |                              |                          |     |  |  |
|    |           | Symbols Define:  |                              |                          |     |  |  |
|    |           | x: Chip length y:  | Chip width z: Chip to        | thickness                |     |  |  |
|    |           | k: Seal width t:   | Glass thickness a: LCD       | side length              |     |  |  |
|    |           | L: Electrode pad length  | ո:                           |                          |     |  |  |
|    |           |  |                              |                          |     |  |  |
|    |           | 6.1 General glass chip   | :                            |                          |     |  |  |
|    |           | 6.1.1 Chip on panel su   | rface and crack between      | panels:                  |     |  |  |
|    |           |  | Y K                          |                          |     |  |  |
|    |           | z: Chip thickness  | y: Chip width                | x: Chip length           |     |  |  |
|    |           | Z≦1/2t   | Not over viewing             | x≦1/8a                   |     |  |  |
| 06 | Chipped   |  | area                         |                          | 2.5 |  |  |
|    | glass     | 1/2t < z ≦ 2t  | Not exceed 1/3k              | x≦1/8a                   |     |  |  |
|    |           | <ul><li>  ⊙If there are 2 or more</li><li>  6.1.2 Corner crack:</li></ul>  | e chips, x is total length o | of each chip.            |     |  |  |
|    | 1         | z: Chip thickness  | y: Chip width                | x: Chip length           |     |  |  |
|    | K 7       | Z≦1/2t   | Not over viewing             | x≦1/8a                   |     |  |  |
|    |           | _= '/  | area                         | Λ <u>=</u> // <b>0</b> α |     |  |  |
|    |           | 1/2t <z≦2t< td=""><td>Not exceed 1/3k</td><td>x≦1/8a</td><td></td></z≦2t<> | Not exceed 1/3k              | x≦1/8a                   |     |  |  |
|    |           | ⊙ If there are 2 or more   | e chips, x is the total leng | yth of each chip.        |     |  |  |



| NO | Item           | Criterion  | AQL      |
|----|----------------|--|----------|
|    |                | Symbols :  |          |
|    |                | x: Chip length y: Chip width z: Chip thickness                               |          |
|    |                | k: Seal width t: Glass thickness a: LCD side length                          |          |
|    |                | L: Electrode pad length  |          |
|    |                | 6.2 Protrusion over terminal :   |          |
|    |                | 6.2.1 Chip on electrode pad :  | <u> </u> |
|    |                |  |          |
|    |                | 2  |          |
|    |                | Ohio width and Ohio Longeth and Ohio Width                                   |          |
|    |                | y: Chip width x: Chip length z: Chip thickness                               |          |
|    |                | $y \le 0.5 \text{mm} \qquad x \le 1/8 \text{a} \qquad 0 < z \le t$           |          |
|    |                | 6.2.2 Non-conductive portion:  |          |
| 06 | Glass<br>crack | y 12 x 12  | 2.5      |
|    |                | y: Chip width x: Chip length z: Chip thickness                               |          |
|    |                | y≦ L   |          |
|    |                | ⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO          |          |
|    | _              | must remain and be inspected according to electrode terminal specifications. |          |
|    |                | ⊙ If the product will be heat sealed by the customer, the alignment          |          |
|    |                | mark not be damaged.   |          |
|    |                | 6.2.3 Substrate protuberance and internal crack.                             |          |
|    |                | y: width x: length   | 7        |
|    |                | $y \le 1/3L$ $x \le a$   | 7        |
|    |                | У  |          |



| NO | Item                  | Criterion   | AQL                 |
|----|-----------------------|---|---------------------|
| 07 | Cracked glass         | The LCD with extensive crack is not acceptable.   | 2.5                 |
| 08 | Backlight<br>elements | <ul> <li>8.1 Illumination source flickers when lit.</li> <li>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.</li> <li>8.3 Backlight doesn't light or color wrong.</li> </ul> | 0.65<br>2.5<br>0.65 |
| 09 | Bezel                 | <ul><li>9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li><li>9.2 Bezel must comply with job specifications.</li></ul>  | 2.5<br>0.65         |
|    |                       | <ul> <li>10.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>10.2 COB seal surface may not have pinholes through to the IC.</li> </ul>   | 2.5                 |
|    |                       | 10.3 The height of the COB should not exceed the height indicated in the assembly diagram.  | 2.5<br>0.65         |
|    |                       | 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.   | 2.5                 |
|    |                       | 10.5 No oxidation or contamination PCB terminals.   |                     |
| 10 | PCB · COB             | 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.  | 2.5<br>0.65         |
|    |                       | <ul><li>10.7 The jumper on the PCB should conform to the product characteristic chart.</li><li>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.</li></ul>                        | 0.65                |
|    |                       | 10.9 The Scraping testing standard for Copper Coating of PCB  | 2.5                 |
|    |                       | X * Y<=2mm2   | 2.5                 |
|    | _                     | 11.1 No un-melted solder paste may be present on the PCB.   | 2.5                 |
| 11 | Soldering             | 11.2 No cold solder joints, missing solder connections, oxidation or icicle.  | 2.5                 |
|    |                       | 11.3 No residue or solder balls on PCB.   | 2.5                 |
|    |                       | 11.4 No short circuits in components on PCB.  | 0.65                |



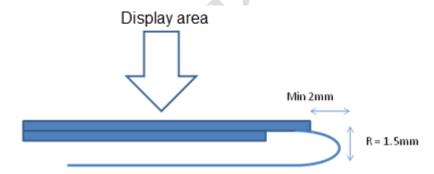


|  |         |   |  |      | _ |
|--|---------|---|--|------|---|
|  | NO      | Item  | Criterion  | AQL  |   |
|  |         | <ul> <li>12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.</li> <li>12.2 No cracks on interface pin (OLB) of TCP.</li> <li>12.3 No contamination, solder residue or solder balls on product.</li> </ul> | 2.5<br>0.65<br>2.5   |      |   |
|  |         |   | 12.4 The IC on the TCP may not be damaged, circuits.   | 2.5  |   |
|  | General |   | 12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to | 2.5  |   |
|  |         | sever.  12.6 The residual rosin or tin oil of soldering (component or chip  | 2.5  |      |   |
|  |         | appearance  | component) is not burned into brown or black color.  | 2.5  |   |
|  |         |   | 12.7 Sealant on top of the ITO circuit has not hardened.   | 0.65 |   |
|  |         |   | 12.8 Pin type must match type in specification sheet.  | 0.65 |   |
|  |         |   | 12.9 LCD pin loose or missing pins.  | 0.65 |   |
|  |         | packaging specification sheet.  | 12.10 Product packaging must the same as specified on packaging specification sheet.   | 0.65 |   |
|  |         |   | 12.11 Product dimension and structure must conform to product specification sheet.   |      |   |
|  |         |   | 12.12 Visual defect outside of VA is not considered to be rejection.   |      |   |



### 11.Precautions in use of LCD Modules

- (1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3)Don't disassemble the LCM.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) Raystar have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- (9)Raystar have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Raystar have the right to modify the version.)
- (10) To ensure the stability of the display screen, please apply screen saver after showing 30 mins of fixed display content.
- (11) The limitation of FPC bending



(12)Please heat up a little the tape sticking on the components when removing it; otherwise the components might be damaged.



### 12. Material List of Components for RoHs

1. RAYSTAR Optronics. Inc. hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A: The Harmful Material List

| Material   | Cd  | Pb   | Hg   | Cr6+ | PBB  | PBDE | DEHP | BBP  | DBP  | DIBP |
|--|-----|------|------|------|------|------|------|------|------|------|
| Limited  | 100 | 1000 | 1000 | 1000 | 1000 | 1000 | 1000 | 1000 | 1000 | 1000 |
| Value  | ppm | ppm  | ppm  | ppm  | ppm  | ppm  | ppm  | ppm  | ppm  | ppm  |
| Above limited value is set up according to RoHS. |     |      |      |      |      |      |      |      |      |      |

- 2.Process for RoHS requirement: (only for RoHS inspection)
  - (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
  - (2) Heat-resistance temp. :

Reflow: 250°C,30 seconds Max.;

Connector soldering wave or hand soldering : 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. :  $235\pm5^{\circ}$ 

Recommended customer's soldering temp. of connector : 280°C, 3 seconds.



# 13. Recommendable Storage

- 1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
- 2. Do not place the module near organics solvents or corrosive gases.
- 3. Do not crush, shake, or jolt the module.



Page: 1

|  | LCM Sample | Estimate Feedback Sheet |  |  |  |
|--|------------|-------------------------|--|--|--|
| Module Number :  |            |                         |  |  |  |
| 1 · Panel Specification :                                    |            |                         |  |  |  |
| 1. Panel Type:   | □ Pass     | □ NG ,                  |  |  |  |
| 2. View Direction:   | □ Pass     | □ NG ,                  |  |  |  |
| 3. Numbers of Dots:  | □ Pass     | □ NG ,                  |  |  |  |
| 4. View Area:  | □ Pass     | □ NG ,                  |  |  |  |
| 5. Active Area :   | □ Pass     | □ NG ,                  |  |  |  |
| 6.Operating Temperature :                                    | □ Pass     | □ NG ,                  |  |  |  |
| 7.Storage Temperature :                                      | □ Pass     | □ NG ,                  |  |  |  |
| 8.Others:  |            |                         |  |  |  |
| 2 · Mechanical Specification :                               |            |                         |  |  |  |
| 1. PCB Size :  | □ Pass     | □ NG ,                  |  |  |  |
| 2.Frame Size :   | □ Pass     | □ NG ,                  |  |  |  |
| 3.Materal of Frame:  | □ Pass     | □ NG ,                  |  |  |  |
| 4.Connector Position:  | □ Pass     | □ NG ,                  |  |  |  |
| 5.Fix Hole Position:   | □ Pass     | □ NG ,                  |  |  |  |
| 6.Backlight Position:  | □ Pass     | □ NG ,                  |  |  |  |
| 7. Thickness of PCB:   | □ Pass     | □ NG ,                  |  |  |  |
| 8. Height of Frame to PCB:                                   | □ Pass     | □ NG ,                  |  |  |  |
| 9.Height of Module:  | □ Pass     | □ NG ,                  |  |  |  |
| 10.Others:   | □ Pass     | □ NG ,                  |  |  |  |
| 3 · Relative Hole Size :                                     |            |                         |  |  |  |
| 1.Pitch of Connector :                                       | □ Pass     | □ NG ,                  |  |  |  |
| 2.Hole size of Connector:                                    | □ Pass     | □ NG ,                  |  |  |  |
| 3.Mounting Hole size:  | □ Pass     | □ NG ,                  |  |  |  |
| 4.Mounting Hole Type:  | □ Pass     | □ NG ,                  |  |  |  |
| 5.Others:  | □ Pass     | □ NG ,                  |  |  |  |
| 4 · Backlight Specification :                                |            |                         |  |  |  |
| 1.B/L Type:  | □ Pass     | □ NG ,                  |  |  |  |
| 2.B/L Color:   | □ Pass     | □ NG ,                  |  |  |  |
| 3.B/L Driving Voltage (Reference for LED Type):□ Pass □ NG , |            |                         |  |  |  |
| 4.B/L Driving Current:                                       | □ Pass     | □ NG ,                  |  |  |  |
| 5.Brightness of B/L:   | □ Pass     | □ NG ,                  |  |  |  |
| 6.B/L Solder Method:   | □ Pass     | □ NG ,                  |  |  |  |
| 7.Others:  | □ Pass     | □ NG ,                  |  |  |  |

>> Go to page 2 <<



Page: 2

| Module Number :                            |        | 95.       |  |  |  |
|--|--------|-----------|--|--|--|
| 5 · Electronic Characteristics of Module : |        |           |  |  |  |
| 1.Input Voltage:                           | □ Pass | □ NG ,    |  |  |  |
| 2.Supply Current:                          | □ Pass | □ NG ,    |  |  |  |
| 3.Driving Voltage for LCD:                 | □ Pass | □ NG ,    |  |  |  |
| 4.Contrast for LCD:                        | □ Pass | □ NG ,    |  |  |  |
| 5.B/L Driving Method:                      | □ Pass | □ NG ,    |  |  |  |
| 6.Negative Voltage Output:                 | □ Pass | □ NG ,    |  |  |  |
| 7.Interface Function:                      | □ Pass | □ NG ,    |  |  |  |
| 8.LCD Uniformity:                          | □ Pass | □ NG ,    |  |  |  |
| 9.ESD test:                                | □ Pass | □ NG ,    |  |  |  |
| 10.Others:                                 | □ Pass | □ NG ,    |  |  |  |
| 6 · <u>Summary</u> :                       |        |           |  |  |  |
| Sales signature :                          |        |           |  |  |  |
| Customer Signature :                       |        | Date: / / |  |  |  |
|  |        | -         |  |  |  |